

ACKNOWLEDGEMENT RECEIPT

Electronic Version

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Title of Invention

METHOD FOR FABRICATING A SEMICONDUCTOR TRANSISTOR
DEVICE HAVING ULTRA-SHALLOW SOURCE/DRAIN
EXTENSIONS

Submission Type : Utility Patent Filing

Application Number:

10/710521



EFS ID:

64741

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First Named Applicant: Yun-Ren Wang

Attorney Docket Number: NAUP0591USA

Timestamp: 2004-07-18 21:39:59 EDT

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File Listing:

Doc. Name	File Name	Size (Bytes)	Date Produced (yyyymmdd)
us-assignment	NAUP0591_0_A1-usassn.xml	2064	2004-07-18
us-assignment	us-assignment.xml	8983	2004-07-18
us-assignment	us-assignment.dtd	10898	2004-07-18
us-assignment	NAUP0591_0_A1ASS1.tif	99432	2004-07-18
us-assignment	NAUP0591_0_A1ASS2.tif	89970	2004-07-18
us-request	NAUP0591_0_A1-usrequ.xml	2909	2004-07-18
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us-request	us-request.xml	33300	2004-07-18
us-fee-sheet	NAUP0591_0_A1-usfees.xml	2130	2004-07-18
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us-fee-sheet	us-fee-sheet.dtd	11069	2004-07-18
us-declaration	NAUP0591_0_A1-usdecl.xml	1162	2004-07-18
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us-declaration	us-declaration.xml	10015	2004-07-18
us-power-of-attorney-grant	NAUP0591_0_A1-uspoat.xml	1049	2004-07-18
us-power-of-attorney-grant	us-power-of-attorney-grant.dtd	4923	2004-07-18
us-power-of-attorney-grant	us-power-of-attorney-grant.xml	12448	2004-07-18
us-declaration	NAUP0591_0_A1DEC1.tif	101964	2004-07-18
us-declaration	NAUP0591_0_A1DEC2.tif	77582	2004-07-18
application-body	NAUP0591-trans.xml	20201	2004-07-18
application-body	us-application-body.xml	83497	2004-07-18
application-body	application-body.dtd	49498	2004-07-18
application-body	wipo.ent	4956	2004-07-18

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application-body	Naup0591usa-02.tif	5088	2004-07-18
application-body	Naup0591usa-03.tif	5014	2004-07-18
application-body	Naup0591usa-04.tif	4690	2004-07-18
application-body	Naup0591usa-05.tif	7188	2004-07-18
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package-data	package-data.dtd	27025	2004-07-18
package-data	us-package-data.xsl	19263	2004-07-18
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